Figure 1 Prior Art

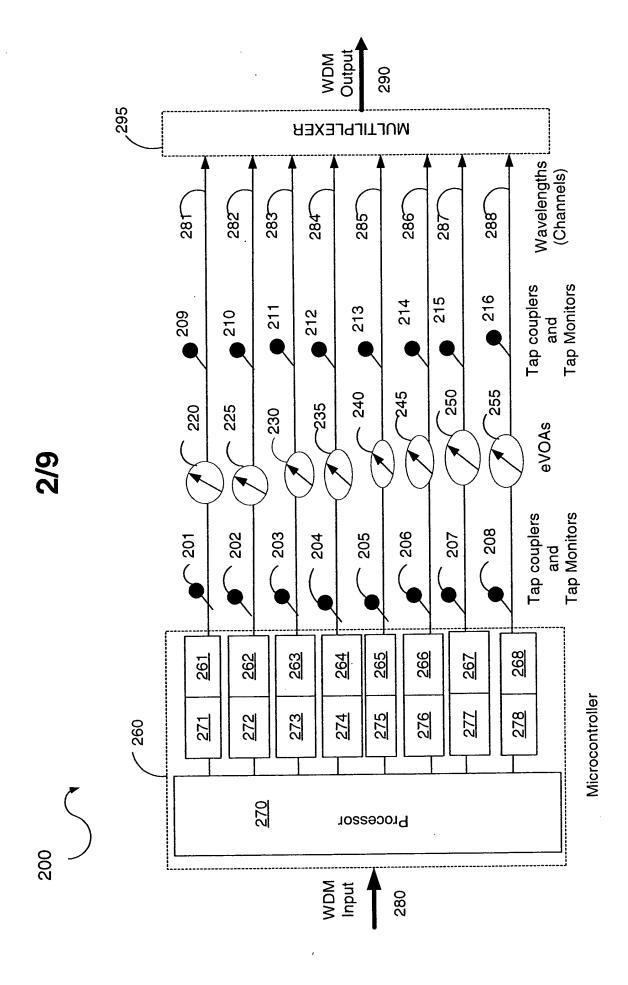


Figure 2 Prior Art

Microprocessing controller

Scheduler

Processor

WDM Input

WDM Output

MULTIPLEXER

Microcontroller

Tap couplers

eVOAs #1 ... #n

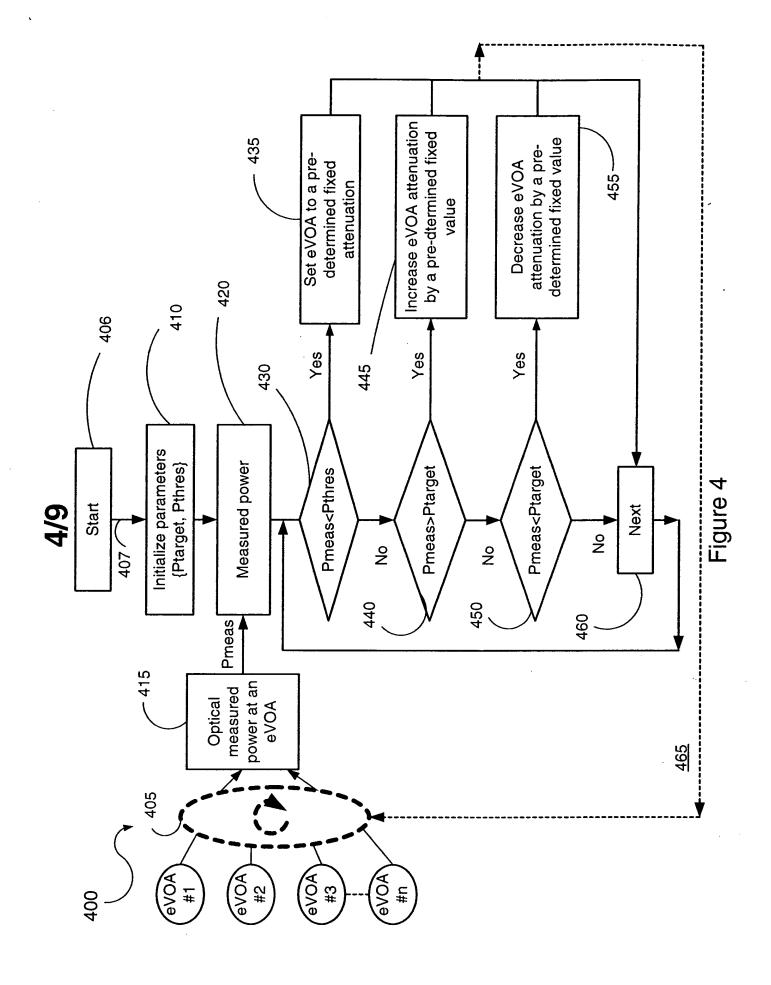
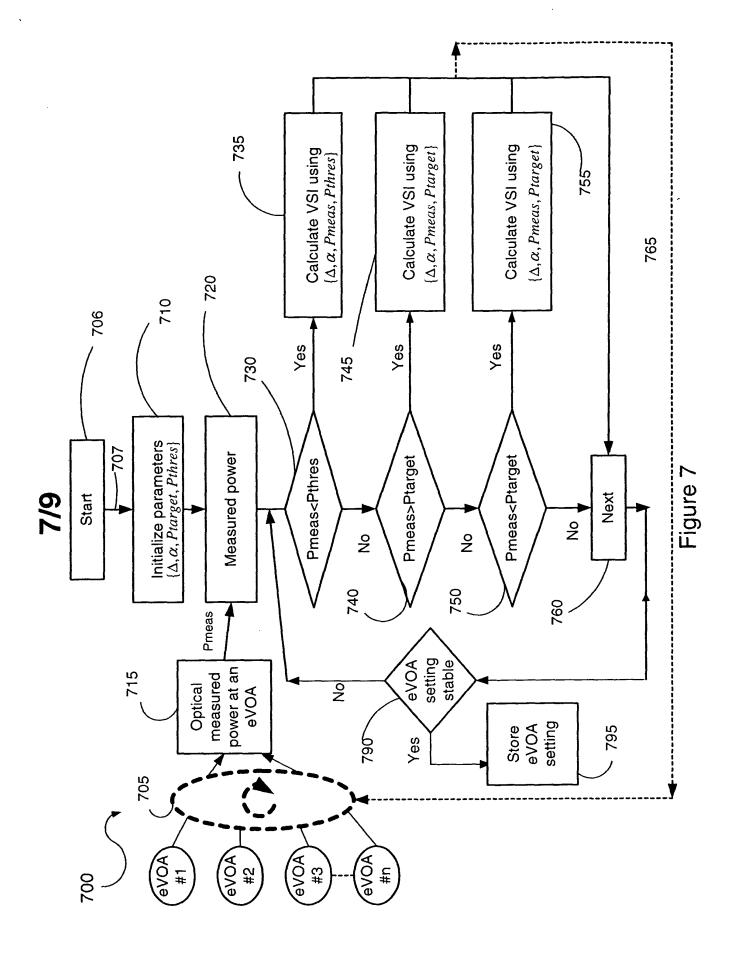
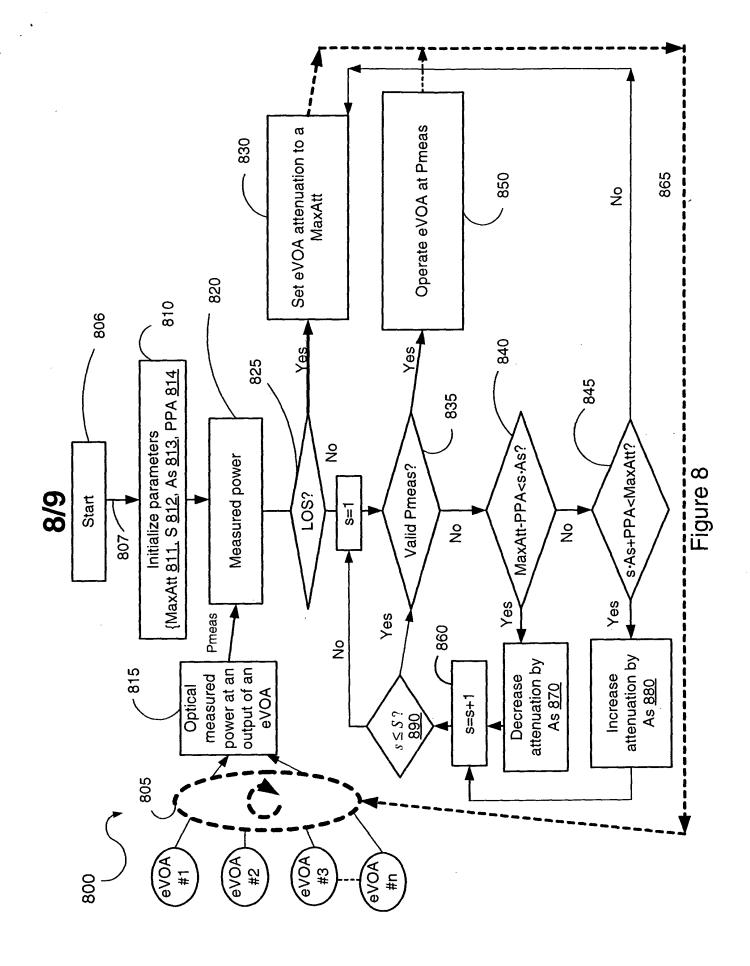


Figure 5

Microcontroller

Figure 6





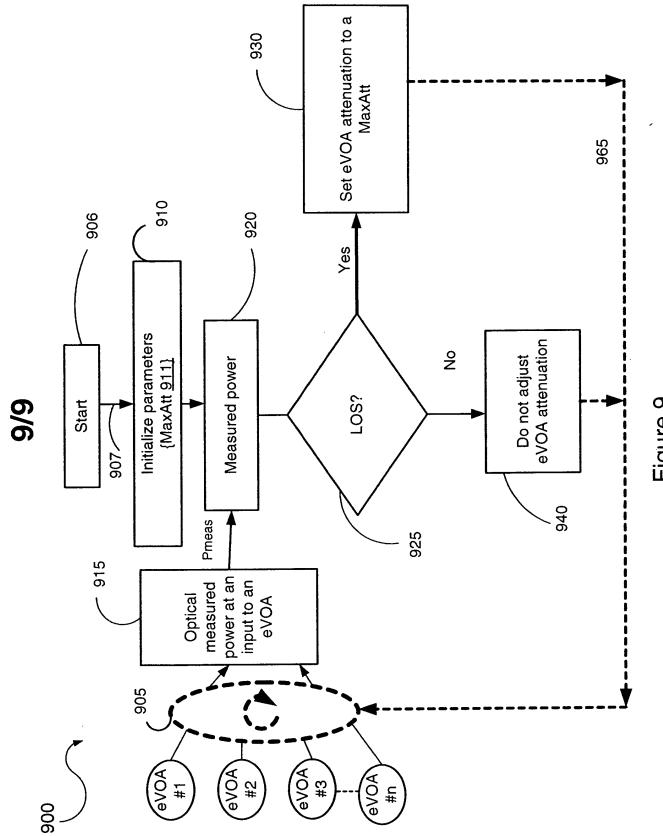


Figure 9